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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10043237	FILING DATE 01/14/2002	CLASS 438	SUBCLASS 197	GAU 288	EXAMINER VESPERMAN
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**APPLICANTS: Doumae Yasuhiro;

**CONTINUING DATA VERIFIED: *we*

** FOREIGN APPLICATIONS VERIFIED: *we*
JAPAN 214613/2001 07/16/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		OKI.293
Verified and Acknowledged Examiners's initials <i>we</i>		
TITLE : Method of manufacturing field effect transistor		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for 0.6
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drawg.	Figs. Drawg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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